Dan Millett is the RIS Deployment manager at ARM where he is responsible for deploying Random Instruction Sequence (RIS) test generators and coverage to both internal and external customers.

He received his B.S. and M.S. from Brigham Young University in Electrical and Computer Engineering. Prior to joining ARM he worked at IBM for three years on unit level x86 processor verification and at Centaur Technology for 16 years doing x86 architectural verification and leading the Instruction Set Simulator (ISS) development.

Dan enjoys spending time with his wife and four children and tries not to bore them with the details of processor verification. He likes spending time outdoors and recently survived a 70 mile backpacking trip with his son at the Philmont Scout Ranch in New Mexico. Other favorite activities include boating on the many lakes in the Austin area and sampling Texas BBQ.