# Embedded Systems Design: A Unified Hardware/Software Introduction

# Chapter 4 Standard Single Purpose **Processors:** Peripherals

# Introduction

- Single-purpose processors
  - Performs specific computation task
  - Custom single-purpose processors
    - Designed by us for a unique task
  - Standard single-purpose processors
    - "Off-the-shelf" -- pre-designed for a common task
    - a.k.a., peripherals
    - · serial transmission
    - · analog/digital conversions
  - Low NRE cost
  - Low unit cost

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#### Timers, counters, watchdog timers

- Timer: measures time intervals very common
  - To generate timed output events
    - e.g., hold traffic light green for 10 s
  - To measure input events
    - e.g., measure a car's speed
- Based on counting clock pulses
  - E.g., let Clk period be 10 ns
  - · And we count 20,000 Clk pulses
  - · Then 200 microseconds have passed
  - 16-bit counter would count up to 65,535\*10 ns = 655.35microsec., resolution = 10 ns
  - · Top: indicates top count reached, wrap-around
    - Can be used to extend range with use of microprocessor

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**Basic time** 16-hit un 16 Cnt Clk counter Reset

Ton

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# Counters

- Counter: like a timer, but counts pulses on a general input signal rather than clock
  - e.g., count cars passing over a sensor
  - Can often configure device as either a timer or counter





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#### Other timer structures



# Example: Reaction Timer



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# Watchdog timer



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# Serial Transmission Using UARTs

UART: Universal Asynchronous Receiver Transmitter embedded device - Takes parallel data and transmits serially - Receives serial data and converts to parallel 1001101 1001101 • Parity: extra bit for simple error Sending UART Receiving UART checking start bit end bit data • Start bit (receiver continually monitors for it), stop bit Baud rate - signal changes per second · Bits shifted out of buffer - Speed of communication 0 1 0 1 1 0 1 1 Configured via configuration register

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#### Pulse width modulator

- Generates pulses with specific ٠ high/low times pwm o • Duty cycle: % time high clk - Square wave: 50% duty cycle
- Common use: control average voltage to electric device
  - Simpler than DC-DC converter or digital-analog converter
  - DC motor speed, dimmer lights
- Another use: encode commands, receiver uses timer to decode

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# Controlling a DC motor with a PWM



# LCD controller



CODES		R	5	R/W	$DB_7$	$DB_6$	DB5	$DB_4$	DB <sub>3</sub>	$DB_2$	DB	$DB_0$	Description
I/D = 1 cursor moves left I/D = 0 cursor moves right	DL = 1 8-bit DL = 0 4-bit		0	0	0	0	0	0	0	0	0	1	Clears all display, return cursor home
= 1 with display shift $N = 1$	N = 1.2 rows		0	0	0	0	0	0	0	0	1	*	Returns cursor home
S/C =1 display shift S/C = 0 cursor movement	N = 0.1 row $F = 1.5 \times 10$ dots $F = 0.5 \times 7$ dots		0	0	0	0	0	0	0	1	I/D	s	Sets cursor move direction and/or specifies not to shift display
R/L = 1 shift to right F			0	0	0	0	0	0	1	D	С	в	ON/OFF of all display(D), cursor ON/OFF (C), and blink position (B)
R/L = 0 shift to left			0	0	0	0	0	1	S/C	R/L	*	*	Move cursor and shifts display
			0	0	0	0	1	DL	N	F	*	*	Sets interface data length, number of display lines, and character font
				0		WRITE DATA						Writes Data	

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### Keypad controller



N=4, M=4

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#### Stepper motor controller

- Stepper motor: rotates fixed number of degrees when given a "step" signal
  - In contrast, DC motor just rotates when power applied, coasts to stop
- Rotation achieved by applying specific voltage sequence to coils
- Controller greatly simplifies this
- If step is 7.5 degrees, but do entire sequence to rotate 7.5 degrees
- Opposite order for opposite direction
- Can use a driver chip or do in software

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# Stepper motor with controller (driver)



Stepper motor without controller (driver)



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### Analog-to-digital converters



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#### DAC/ADC conversion

• Using ratio:

e = present analog voltage  $e/V_{max} = d/(2^{n}-1)$ d = digital encoding

n = number of bits

Assume V<sub>min</sub> - 0

- Resolution is the number of volts between successive digital encodings
- DACs are easy: input *d* for digital encoding and max voltage and output analog e using resistors and op-amp
- ADCs are hard: Given  $V_{max}$  and *e* how does converter know the binary value to assign to satisfy the ratio?
  - No simple analog circuit

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- ADC
- ADCs may contain DACs
- ADC guesses at encoding and then evaluates its guess using the DAC
- So how do we guess the correct encoding?
  - Sequential search? Too slow with 2<sup>n</sup> encodings
  - Binary search?
- ADCs take a bit of time to get correct encoding

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# Digital-to-analog conversion using successive approximation

Given an analog input signal whose voltage should range from 0 to 15 volts, and an 8-bit digital encoding, calculate the correct encoding for 5 volts. Then trace the successive-approximation approach to find the correct encoding

5/15 = d/(28-1)d= 85 Encoding: 01010101 Successive-approximation method  $V_2(V_{max} - V_{min}) = 7.5$  volts  $V_{max} = 7.5$  volts. 1/2(5.63 + 4.69) = 5.16 volts 0 1 0 1 0 0 0 0  $V_{max} = 5.16$  volts. <sup>1</sup>/<sub>2</sub>(7.5 + 0) = 3.75 volts 1/2(5.16 + 4.69) = 4.93 volts 0 1 0 0 0 0 0 0 0 1 0 1 0 1 0 0 V<sub>min</sub> = 3.75 volts. V<sub>min</sub> = 4.93 volts. <sup>1</sup>/<sub>2</sub>(7.5 + 3.75) = 5.63 volts 1/2(5.16 + 4.93) = 5.05 volts 0 1 0 0 0 0 0 0 0 1 0 1 0 1 0 0 V<sub>max</sub> = 5.63 volts Vmax = 5.05 volts.  $\frac{1}{2}(5.63 + 3.75) = 4.69$  volts 0 1 1/2(5.05 + 4.93) = 4.99 volts 0 1 0 1 0 1 0 1 V<sub>min</sub> = 4.69 volts.

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